# MALLA REDDY TECHNICAL CAMPUS

A Constituent Unit of MALLA REDDY VISHWAVIDYAPEETH
*(Deemed to be University)*
Approved by UGC & AICTE New Delhi
Maisammaguda, Medchal (Dist), Hyderabad - 500100, Telangana

## B.TECH II YEAR – I SEMESTER (MRV-25) REGULAR EXAMINATIONS NOV-2026

**TIMETABLE**

**TIME→ FN: 10.00 AM TO 12.00 PM AN: 1.30 PM TO 3.30 PM**

| BRANCH | SESSION | 02-11-2026 MONDAY | 4-11-2026 WEDNESDAY | 6-11-2026 FRIDAY | 10-11-2026 TUESDAY | 12-11-2026 THURSDAY | 14-11-2026 SATURDAY |
|---|---|---|---|---|---|---|---|
| **CSE** | **FN** | Probability and Statistics | Design and Analysis of Algorithms | Advanced Communication Skills | Digital Electronics | Back End Web Technologies | Logical Reasoning and Analytical Skills |
| **CSE (AIML)** | **FN** | Design and Analysis of Algorithms | Advanced Communication Skills | Digital Electronics | Back End Web Technologies | Logical Reasoning and Analytical Skills | Probability and Statistics |
| **CSE (DS)** | **AN** | Probability and Statistics | Design and Analysis of Algorithms | Advanced Communication Skills | Digital Electronics | Back End Web Technologies | Logical Reasoning and Analytical Skills |
| **CSE (AI)** | **AN** | Design and Analysis of Algorithms | Advanced Communication Skills | Digital Electronics | Back End Web Technologies | Logical Reasoning and Analytical Skills | Probability and Statistics |
| **CSE (AIDS)** | **AN** | Design and Analysis of Algorithms | Advanced Communication Skills | Digital Electronics | Back End Web Technologies | Logical Reasoning and Analytical Skills | Probability and Statistics |
| **CSE (IOT & CSBT)** | **AN** | Design and Analysis of Algorithms | Advanced Communication Skills | Digital Electronics | Back End Web Technologies | Logical Reasoning and Analytical Skills | Probability and Statistics |
| **CSE (CS)** | **AN** | Design and Analysis of Algorithms | Advanced Communication Skills | Digital Electronics | Back End Web Technologies | Logical Reasoning and Analytical Skills | Probability and Statistics |
| **CSIT** | **AN** | Design and Analysis of Algorithms | Advanced Communication Skills | Digital Electronics | Back End Web Technologies | Logical Reasoning and Analytical Skills | Probability and Statistics |
| **ECE** | **AN** | Digital System Design | Signals and Systems | Linear and Digital IC Applications | Electronic Circuit Analysis | Logical Reasoning and Analytical Skills | *** |
| **R & AI** | **AN** | Theory of Mechanisms and Machines | Actuators and Drives | Sensors and Signal Processing | Artificial Intelligence | Introduction to Drones | *** |

21/9/2026

PRO-VICE CHANCELLOR

Pro Vice Chancellor
Malla Reddy Technical Campus
A Constituent Unit of Malla Reddy Vishwavidyapeeth